

INNOLUX DISPLAY CORPORATION

LCD MODULE

SPECIFICATION

Customer: _____
Model Name: NJ070NA-23A
Date: 2021/11/17
Version: 07

☐ **Preliminary Specification**
☒ **Final Specification**

For Customer's Acceptance

Approved by	Comment

Approved by	Reviewed by	Prepared by
Joko.Wang 2021/11/17	Menghsuan.Lu 2021/11/17	Chien.Chan 2021/11/17

Record of Revision

Version	Revise Date	Page	Content
Pre-Spec.V01	2014/04/15	All	Initial Release.
Pre-Spec.V02	2014/12/23	1	Update Backlight power consumption& Panel power consumption& Weight
		5	Update Operation Specifications
		6	Update Input signal voltage
		7	Update Current Consumption& Backlight Driving Conditions
		13	Update Optical Specifications
		20	Update Packaging Material Table
Pre-Spec.V03	2015/01/09	7	Update Current Consumption
		18	Update Safety
		19	Modify Mechanical Drawing
		21	Modify Package Drawing
Final-Spec	2015/04/20		First Final Release for P070BAG-CM1
		19	Modify Mechanical Drawing
	2016/01/07	19	Modify Mechanical Pixel size.
Final-Spec-V01	2016/12/27	1	Model name change from P070BAG-CM1 to NJ070NA-23A
Final-Spec-V02	2017/02/27	20	Modify a slip of the pen of Reliability
Final-Spec-V03	2017/05/22	1	General specifications
		5~12	Update Operation Specifications
Final-Spec-V04	2019/03/18	24~25	Modify Packing Drawing
Final-Spec-V05	2019/12/19	20	Mechanical Drawing
		21	Modify Packing Drawing
Final-Spec-V06	2021/09/30	1,3,5,6, 10~14, 20	Change Panel & IC Modify: General specifications Pin Assignment Operation Specifications Optical Specifications Mechanical Drawing
Final-Spec-V07	2021/11/17	20	Mechanical Drawing

Contents

1. General Specifications	1
2. Pin Assignment	2
3. Operation Specifications	5
3.1. Absolute Maximum Ratings	5
3.1.1. Typical Operation Conditions	6
3.1.2. Current Consumption	7
3.1.3. Backlight Driving Conditions	7
3.2. Power Sequence	8
3.3. Timing Characteristics	10
3.3.1. AC Electrical Characteristics	10
3.3.2. Input Clock and Data Timing Diagram	10
3.3.3. DC Electrical Characteristics	11
3.3.4. Timing	12
3.3.5. Data Input Format	13
4. Optical Specifications	14
5. Reliability Test Items	18
6. General Precautions	19
6.1. Safety	19
6.2. Handling	19
6.3. Static Electricity	19
6.4. Storage	19
6.5. Cleaning	19
7. Mechanical Drawing	20
8. Package Drawing	21

1. General Specifications

The specification NJ070NA-23A is a 7 "(1024x600) TFT Liquid Crystal Display module with LED Backlight unit , 40 pin LVDS interface, normally white transmissive display mode. This module will be applied to Consumer Electronics(CE Application)

No.	Item	Specification	Remark
1	LCD size	7.0 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	1024 × 3(RGB) × 600	
4	Display mode	Normally White, Transmissive	
5	Pixel size	0.1506(W) × 0.1432(H) mm	
6	Active area	154.2144(W) × 85.92(H) mm	
7	Module size	164.9(W) × 100(H) × 4.8(D) mm	Note 1
8	Surface treatment	Hard Coating	
9	Color arrangement	RGB-stripe	
10	Interface	LVDS	
11	View direction(Gray Inversion)	6 O'Clock	
12	Backlight power consumption	2.62 W (Typ.)	
13	Panel power consumption	0.4 W (Typ.)	
14	Weight	140g (Typ.)	
15	Source Driver IC	EK79001AX	
16	Gate Driver IC	EK73215BCGA	
17	Driving methods	1+2 dot	

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

FPC Connector is used for the module electronics interface. The recommended model is FH12A-40S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	P	Common Voltage	
2	VDD	P	Power Voltage for digital circuit	
3	VDD	P	Power Voltage for digital circuit	
4	NC	---	No connection	
5	Reset	I	Global reset pin	
6	STBYB	I	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z	
7	GND	P	Ground	
8	RXIN0-	I	- LVDS differential data input	
9	RXIN0+	I	+ LVDS differential data input	
10	GND	P	Ground	
11	RXIN1-	I	- LVDS differential data input	
12	RXIN1+	I	+ LVDS differential data input	
13	GND	P	Ground	
14	RXIN2-	I	- LVDS differential data input	
15	RXIN2+	I	+ LVDS differential data input	
16	GND	P	Ground	
17	RXCLKIN-	I	- LVDS differential clock input	
18	RXCLKIN+	I	+ LVDS differential clock input	
19	GND	P	Ground	
20	RXIN3-	I	- LVDS differential data input	
21	RXIN3+	I	+ LVDS differential data input	

22	GND	P	Ground	
23	NC	---	No connection	
24	NC	---	No connection	
25	GND	P	Ground	
26	NC	---	No connection	
27	NC	---	No connection	
28	SELB	I	6bit/8bit mode select	Note1
29	AVDD	P	Power for Analog Circuit	
30	GND	P	Ground	
31	LED-	P	LED Cathode	
32	LED-	P	LED Cathode	
33	L/R	I	Horizontal inversion	Note2
34	U/D	I	Vertical inversion	Note2
35	VGL	P	Gate OFF Voltage	
36	NC	---	No connection	
37	NC	---	No connection	
38	VGH	P	Gate ON Voltage	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

I: input, O: output, P: Power

Note1: If LVDS input data is 6 bits ,SELB must be set to High;

If LVDS input data is 8 bits ,SELB must be set to Low.

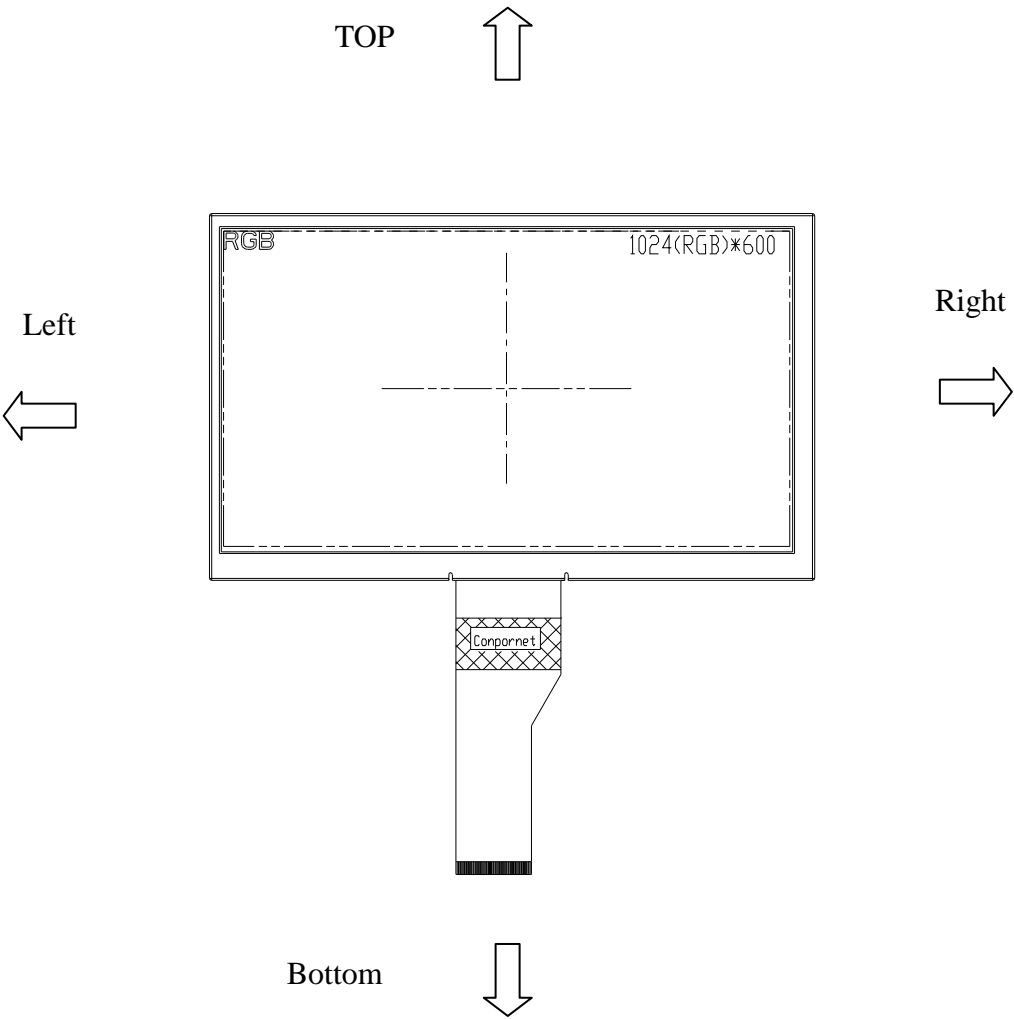
Note2: When L/R="0", set right to left scan direction.

When L/R="1", set left to right scan direction.

When U/D="0", set top to bottom scan direction.

When U/D="1", set bottom to top scan direction.

Note: Definition of scanning direction.
Refer to the figure as below:



3. Operation Specifications

3.1. Absolute Maximum Ratings

(Note 1)

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	DV _{DD}	-0.3	5.0	V	
	AV _{DD}	-0.3	15	V	
	V _{GH}	-0.3	42.0	V	
	V _{GL}	-20.0	0.3	V	
	V _{GH} -V _{GL}	-0.3	40.0	V	
Operation Temperature	T _{OP}	-20	70	°C	
Storage Temperature	T _{ST}	-30	80	°C	
LED Reverse Voltage	V _R	-	5	V	Each LED
LED Forward Current	I _F	-	60	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

3.1.1. Typical Operation Conditions

(Note 1)

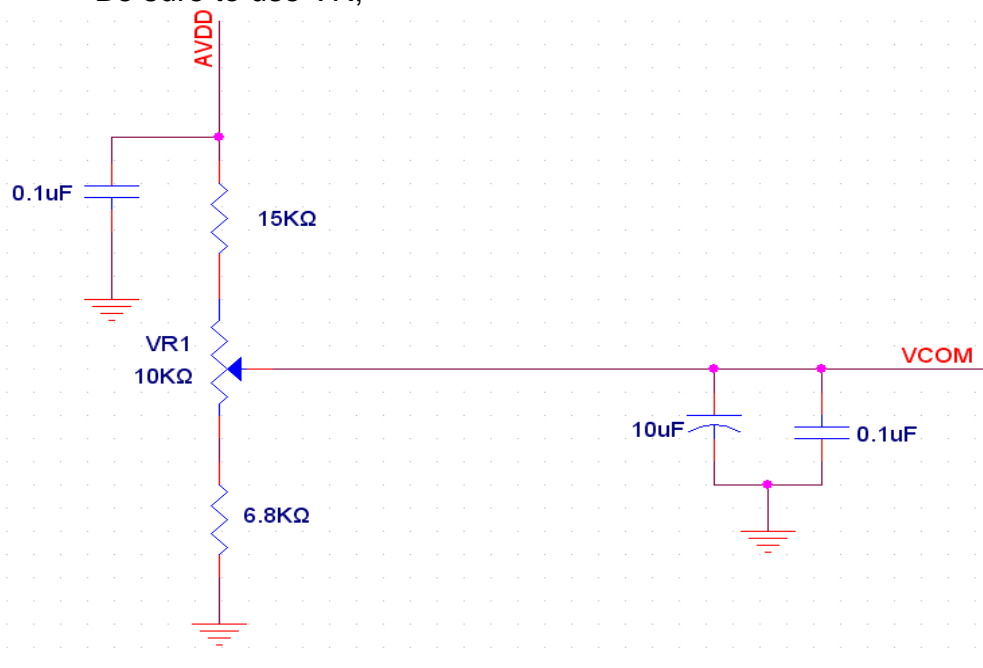
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	DV _{DD}	3.0	3.3	3.6	V	Note 2
	AV _{DD}	11.9	12	12.1	V	
	V _{GH}	14.5	15	15.5	V	
	V _{GL}	-10.5	-10	-9.5	V	
Input signal voltage	V _{COM}	4.05	4.55	5.05	V	Note 4
Input logic high voltage	V _{IH}	0.7 DV _{DD}	-	DV _{DD}	V	Note 3
Input logic low voltage	V _{IL}	0	-	0.3 DV _{DD}	V	

Note 1: Be sure to apply DV_{DD} and V_{GL} to the LCD first, and then apply V_{GH}.

Note 2: DV_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: LVDS, Reset, **STBYB**, **SELB,L/R,U/D**

Note 4: Typ. V_{COM} is only a reference value, it must be optimized according to each LCM.
Be sure to use VR;



3.1.2. Current Consumption

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	I_{GH}	0.05	0.26	0.55	mA	$V_{GH} = 20V$
	I_{GL}	0.05	0.26	0.55	mA	$V_{GL} = -6.8V$
	IDV_{DD}	16	35	55	mA	$DV_{DD} = 3.3V$
	IAV_{DD}	4	20	38	mA	$AV_{DD} = 11V$

3.1.3. Backlight Driving Conditions

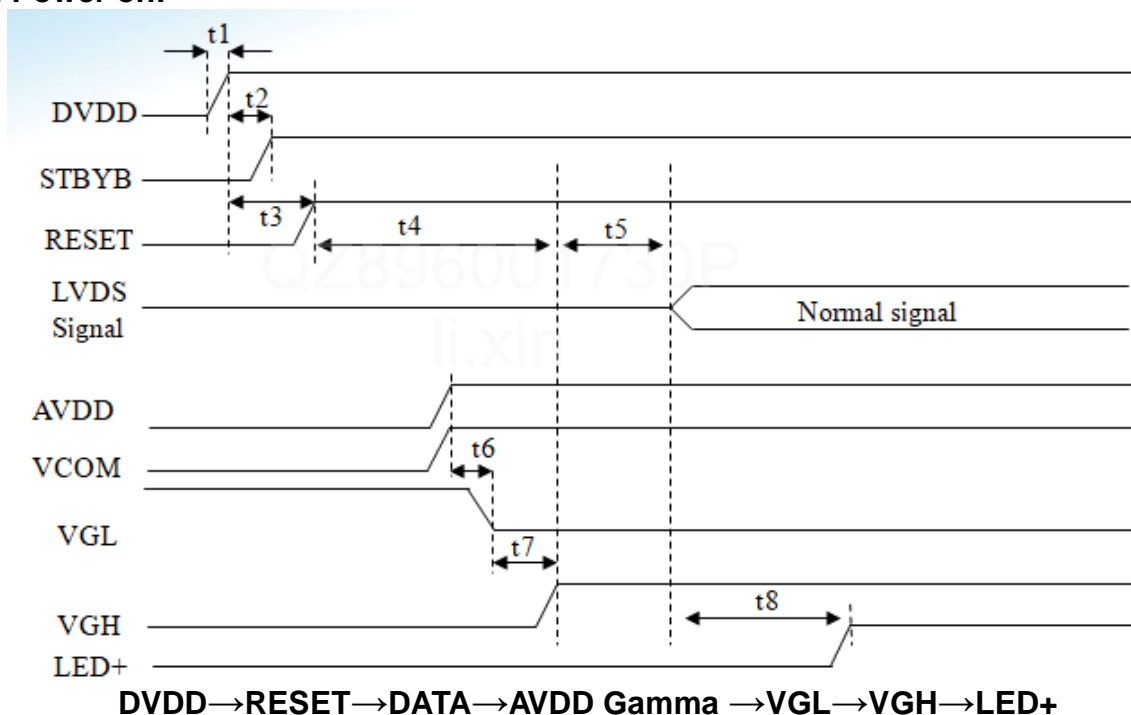
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED backlight	V_L	8.4	9.9	10.2	V	Note 1
Current for LED backlight	I_L	--	265	--	mA	
LED life time	-	20,000	-	-	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at $T_a = 25^{\circ}C$ and $I_L = 265mA$.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at $T_a = 25^{\circ}C$ and $I_L = 265mA$. The LED lifetime could be decreased if operating I_L is larger than 265mA.

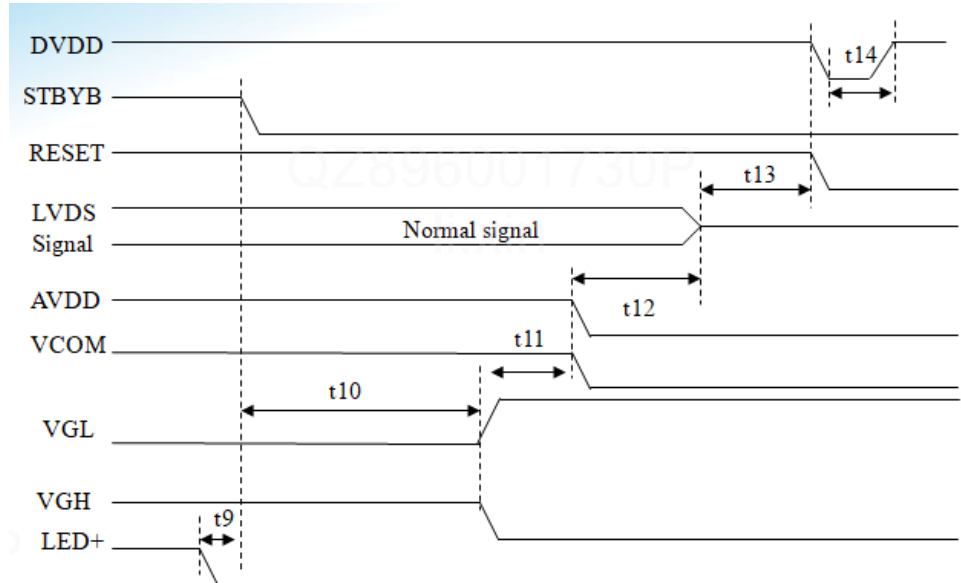
3.2. Power Sequence

a. Power on:



Symbol	SPEC			Unit
	Min.	Typ.	Max.	
t1	0.5	5	20	ms
t2	0	0.25	0.5	ms
t3	1	10(RC Delay)	12	ms
t4	90	-	150	ms
t5	30	-	50	ms
t6	20	-	75	ms
t7	20	-	75	ms
t8	150	-	-	ms

b. Power off:



LED+ → VGL\VGH → AVDD Gamma → DATA → RESET → DVDD

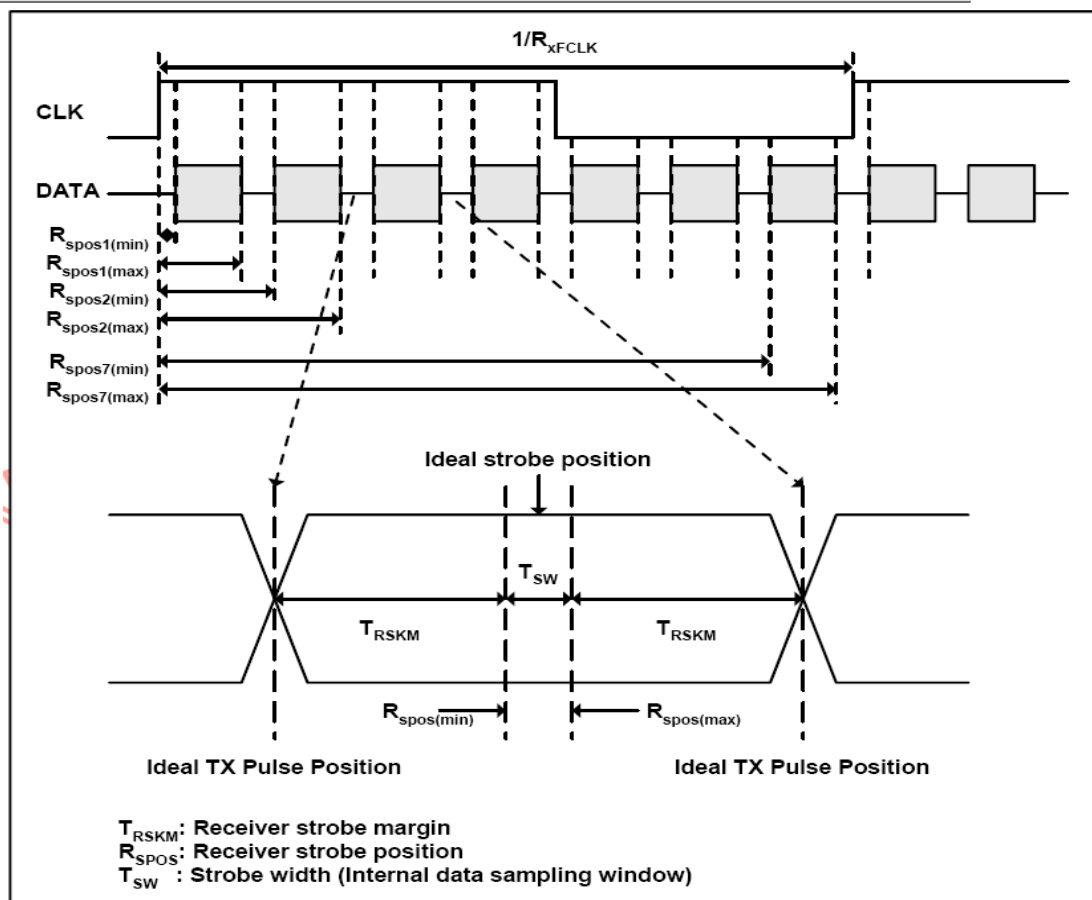
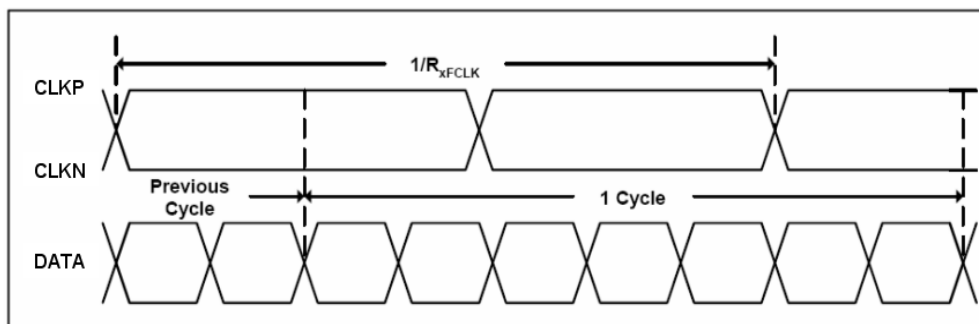
Symbol	SPEC			Unit
	Min.	Typ.	Max.	
t9	0.1	1	10	ms
t10	120	150	200	ms
t11	50	100	200	ms
t12	1	10	20	ms
t13	0.1	10	100	ms
t14	500	-	-	ms

3.3. Timing Characteristics

3.3.1. AC Electrical Characteristics

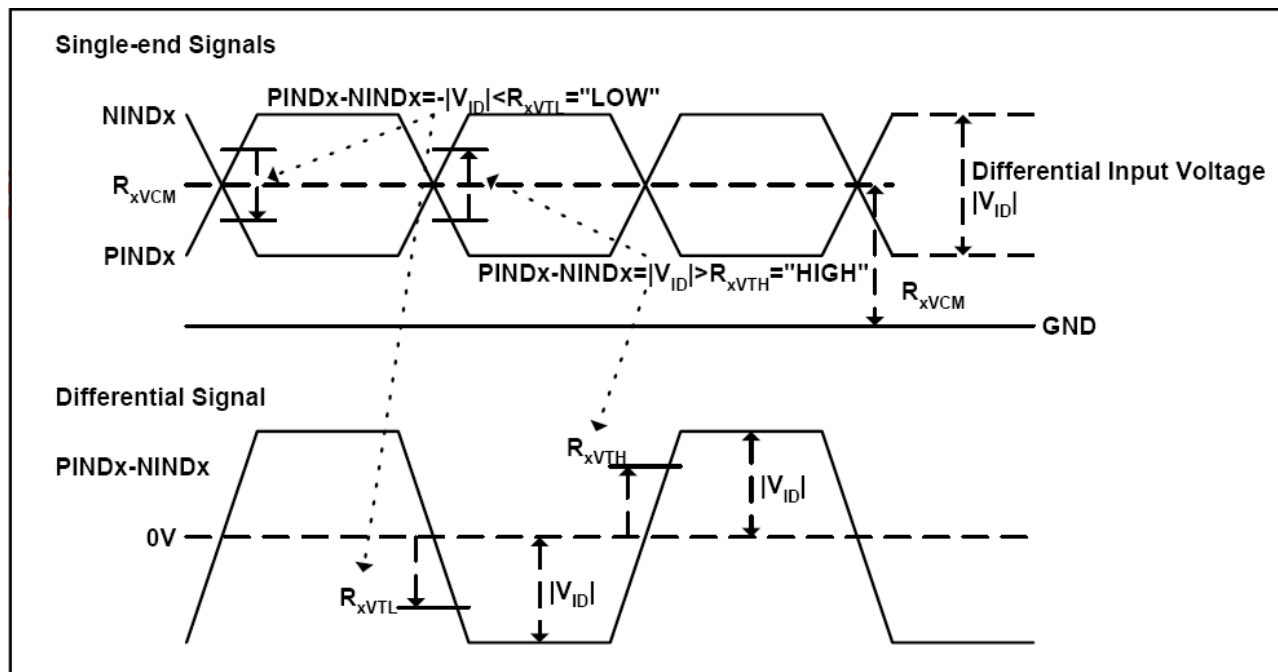
Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock frequency	R_{xFCLK}	40.8	51.2	67.2	MHz	
Input data skew margin	T_{RSKM}	500	900	1200	ps	
Clock high time	T_{LVCH}	$3.8/(7 * R_{xFCLK})$	$4/(7 * R_{xFCLK})$	$4.2/(7 * R_{xFCLK})$	ns	
Clock low time	T_{LVCL}	$2.8/(7 * R_{xFCLK})$	$3/(7 * R_{xFCLK})$	$3.2/(7 * R_{xFCLK})$	ns	

3.3.2. Input Clock and Data Timing Diagram



3.3.3. DC Electrical Characteristics

Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Differential input high Threshold voltage	R_{xVTH}	-	-	0.1	V	$R_{xVCM}=1.2V$
Differential input low Threshold voltage	R_{xVTL}	-0.1	-	-	V	
Input voltage range (singled-end)	R_{xVIN}	0	-	2.4	V	
Differential input common mode voltage	R_{xVCM}	$ V_{ID} /2$	-	$2.4- V_{ID} /2$	V	
Differential voltage	$ V_{ID} $	0.2	-	0.6	V	
Differential input leakage current	RV_{xliz}	-10	-	+10	uA	

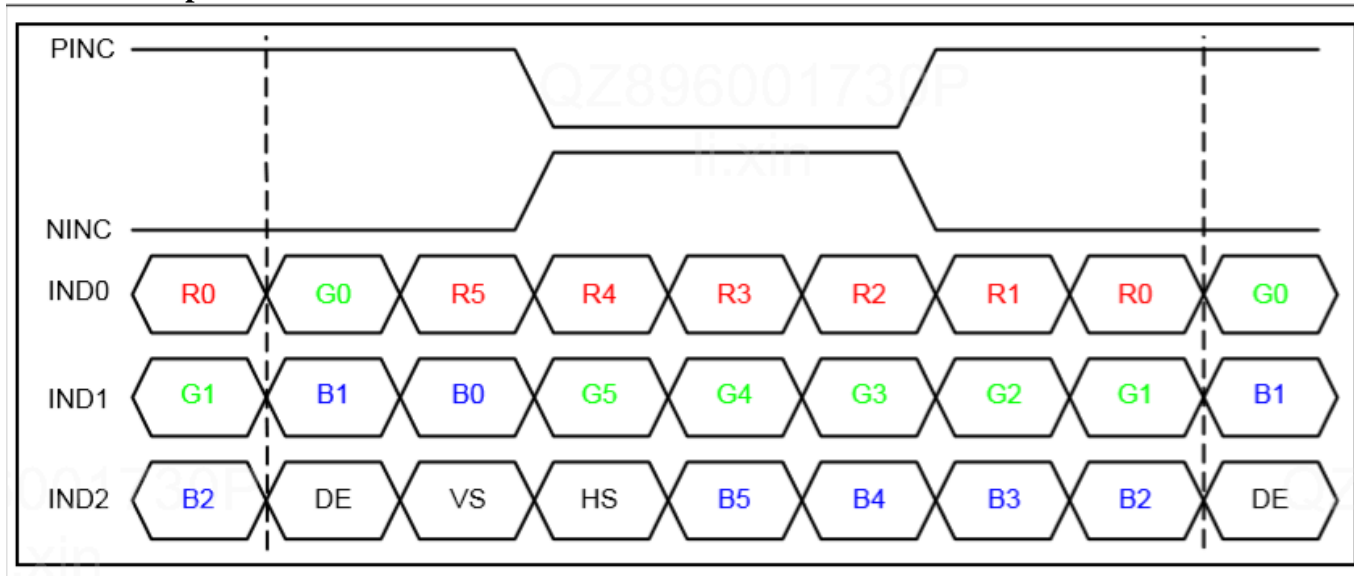


3.3.4. Timing

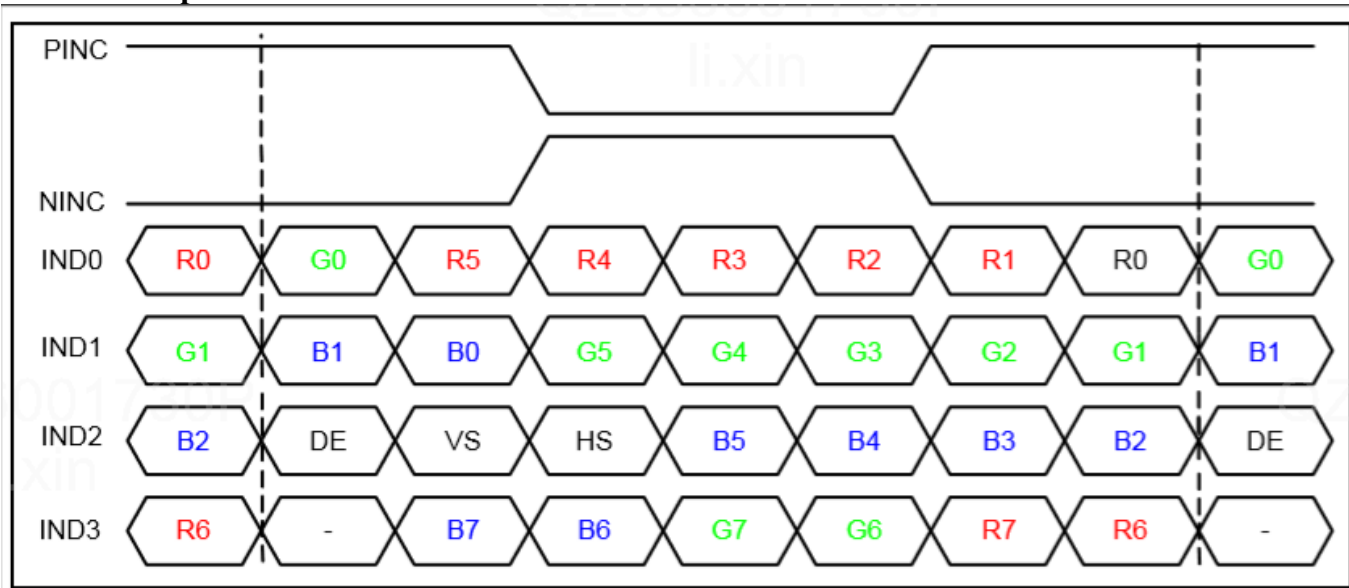
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock Frequency	fclk	40.8	51.2	67.2	MHz	Frame rate =60Hz
Horizontal display area	thd	1024			DCLK	
HS period time	th	1114	1344	1400	DCLK	
HS Blanking	thb	90	320	376	DCLK	
Vertical display area	tvd	600			H	
VS period time	tv	610	635	800	H	
VS Blanking	thb	10	35	200	H	

3.3.5. Data Input Format

6bit LVDS input



8bit LVDS input



Note: Support DE timing mode only, SYNC mode not supported.

4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR \geq 10)	θ_L	$\Phi=180^\circ$ (9 o'clock)	75	80	-	degree	Note 1
	θ_R	$\Phi=0^\circ$ (3 o'clock)	75	80	-		
	θ_T	$\Phi=90^\circ$ (12 o'clock)	55	60	-		
	θ_B	$\Phi=270^\circ$ (6 o'clock)	65	70	-		
Response time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		500	800	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2 Note 5
	W_Y		0.28	0.33	0.38	-	Note 6
Luminance	L		400	500	-	cd/m ²	Note 6
Luminance uniformity	Y_U		70	75	-	%	Note 7

Test Conditions:

1. $DV_{DD}=3.1V$, $I_L=265mA$ (Backlight current), the ambient temperature is $25^\circ C$.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

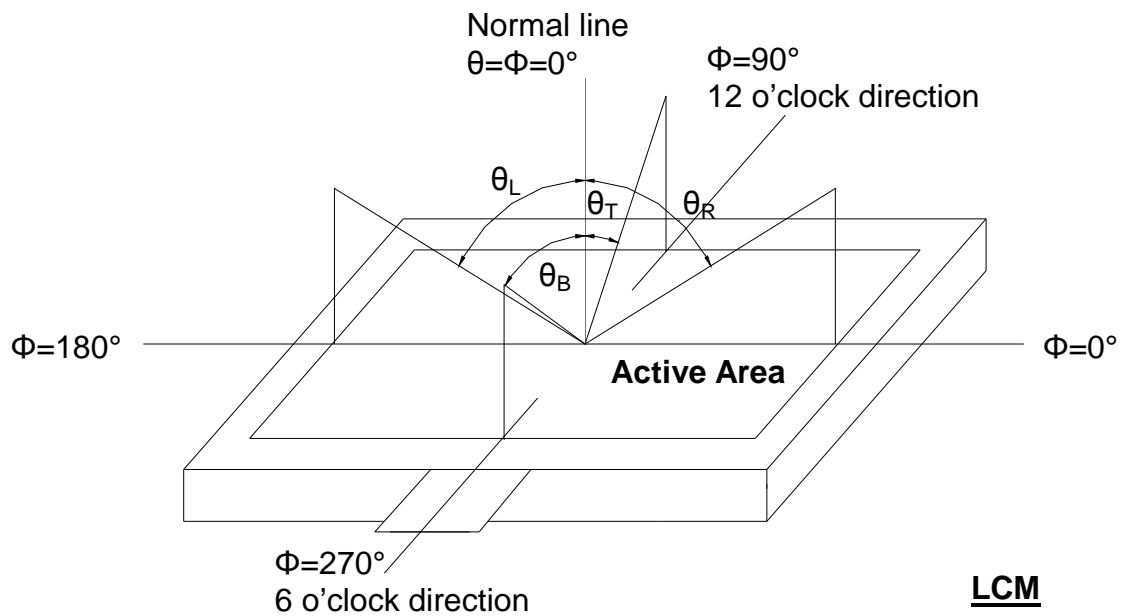


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

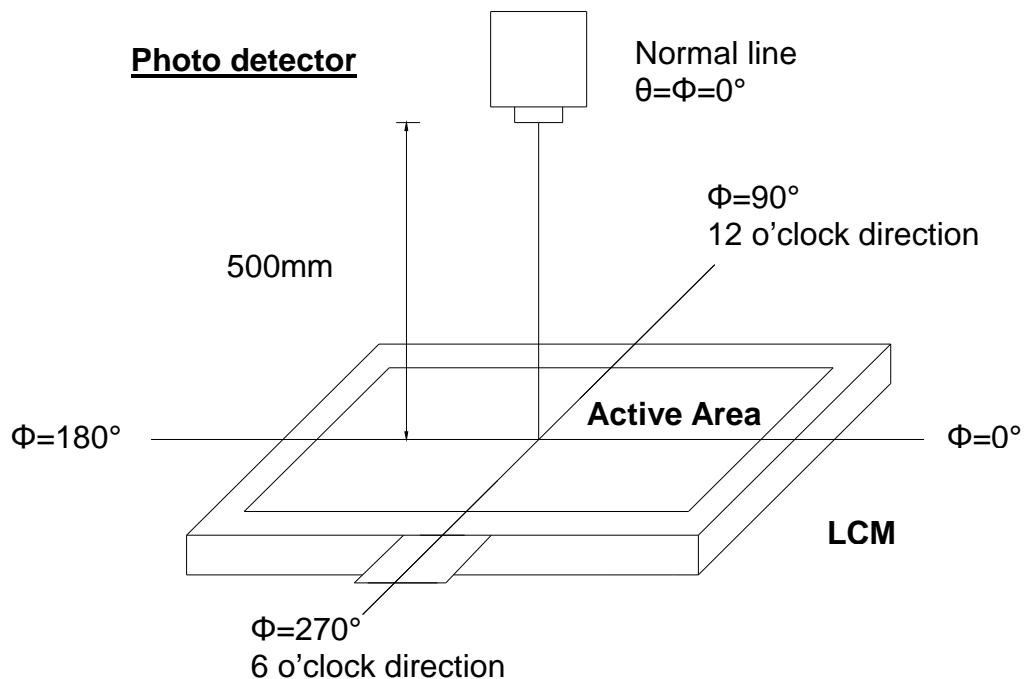


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

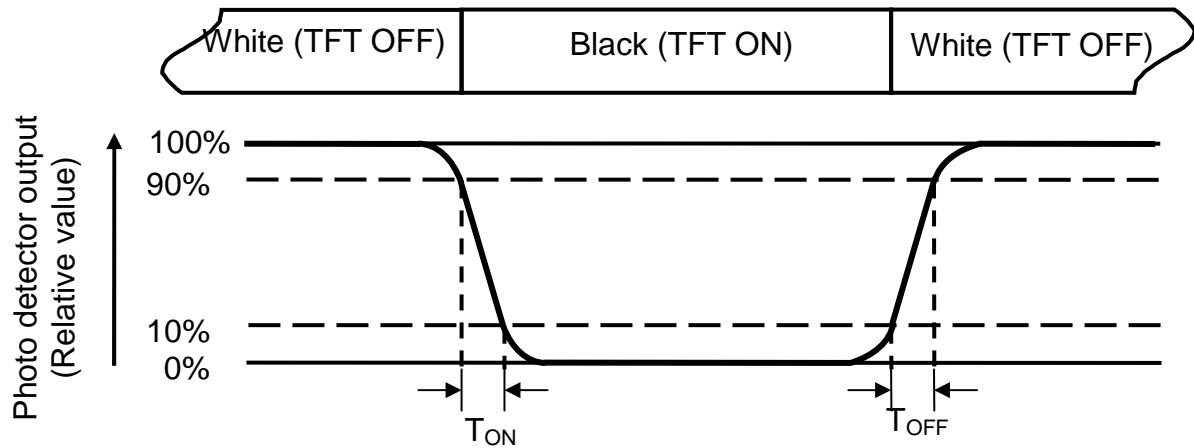


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Contrast ratio is measured at center point of LCD.

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of luminance:

Measured at the center area of the panel when LCD panel is driven at "white" state. The LED driving condition is $I_L=265\text{mA}$.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{min}}{B_{max}}$$

L-----Active area length W----- Active area width

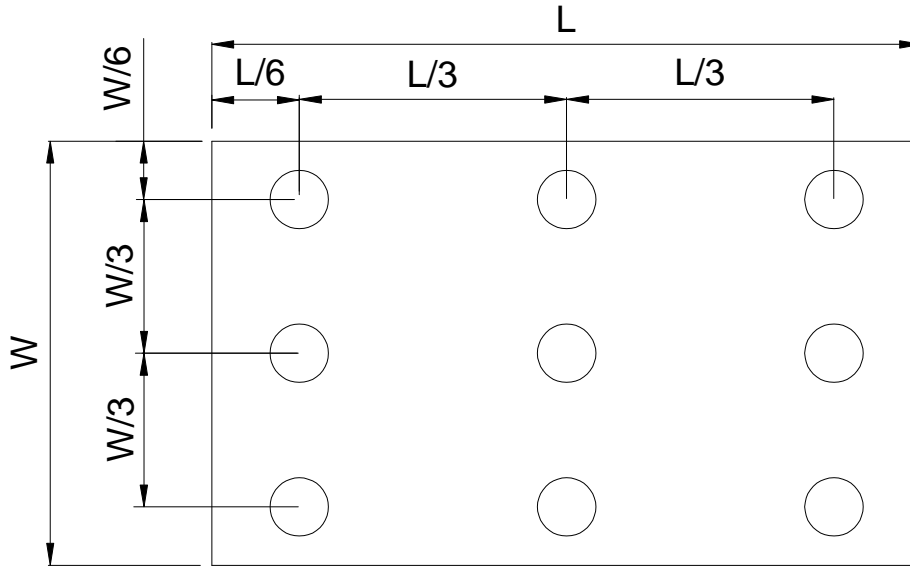


Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.

B_{min}: The measured minimum luminance of all measurement position.

5. Reliability Test Items

Item	Test Conditions	Remark
High Temperature Storage	Ta = 80℃ 240hrs	Note 1, Note3, Note 4, Note5
Low Temperature Storage	Ta = -30℃ 240hrs	Note 1, Note3, Note 4
High Temperature Operation	Ts = 70℃ 240hrs	Note 2, Note3, Note 4, Note5
Low Temperature Operation	Ta = -20℃ 240hrs	Note 1, Note3, Note 4
Operate at High Temperature and Humidity	+60℃, 90%RH 240hrs	Note3, Note 4 Note5
Thermal Shock	[(-30℃ 30min)→(80℃ 30min)]/cycle , 100cycles	Note3, Note 4 Note5
Vibration Test	Frequency: 10 ~55~10Hz; Sweep Mode: Log Sweep Sweep time: 1Oct/min; Acceleration: 1.5G; Test time: 2hrs for each direction of X, Y, Z.	
Mechanical Shock	100G, 6ms, half sine wave, 3 times for each direction of ±X, ±Y, ±Z	
Package Vibration Test	Random Vibration : ISTA-3A 1Hz~200Hz, Grms=0.53 Half hours for direction of Z	
Package Drop Test	72cm(weight ≤ 10kg), 60cm(weight > 10kg); 1 times for 6-faces, 3-edges and 1-corner	
Electro Static Discharge	Contact mode : 150pf , 330Ω , ±2KV Air mode : 150pf , 330Ω , ±2KV	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 5: A certain level of Mura (non-uniformity) of dark / black image will happen several days after high temperature testing (H.T.T.). There is a slowly part recovery over a long time (several months). Such a long exposure time like in H.T.T. will normally not happen in a real application. Therefore the test H.T.T. was introduced to simulate cycles with normal conditions in-between but with the same total exposure time what show a significant reduced Mura.

The root cause is related to tension generated due to different amount of shrinking in the stack of layers in the polarizer sheet. The effect is more significant on larger displays like this size. An investigation into alternative polarizer material showed that there is no better alternative currently available.

6. General Precautions

6.1. Safety

1. Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.
2. The temperature for use is no more than 80 C, otherwise, only promise the function is OK, but the quality may be changed.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

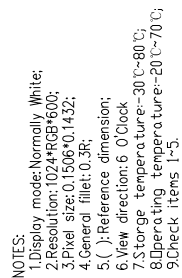
1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

1. Store the module in a dark room where must keep at $25\pm10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

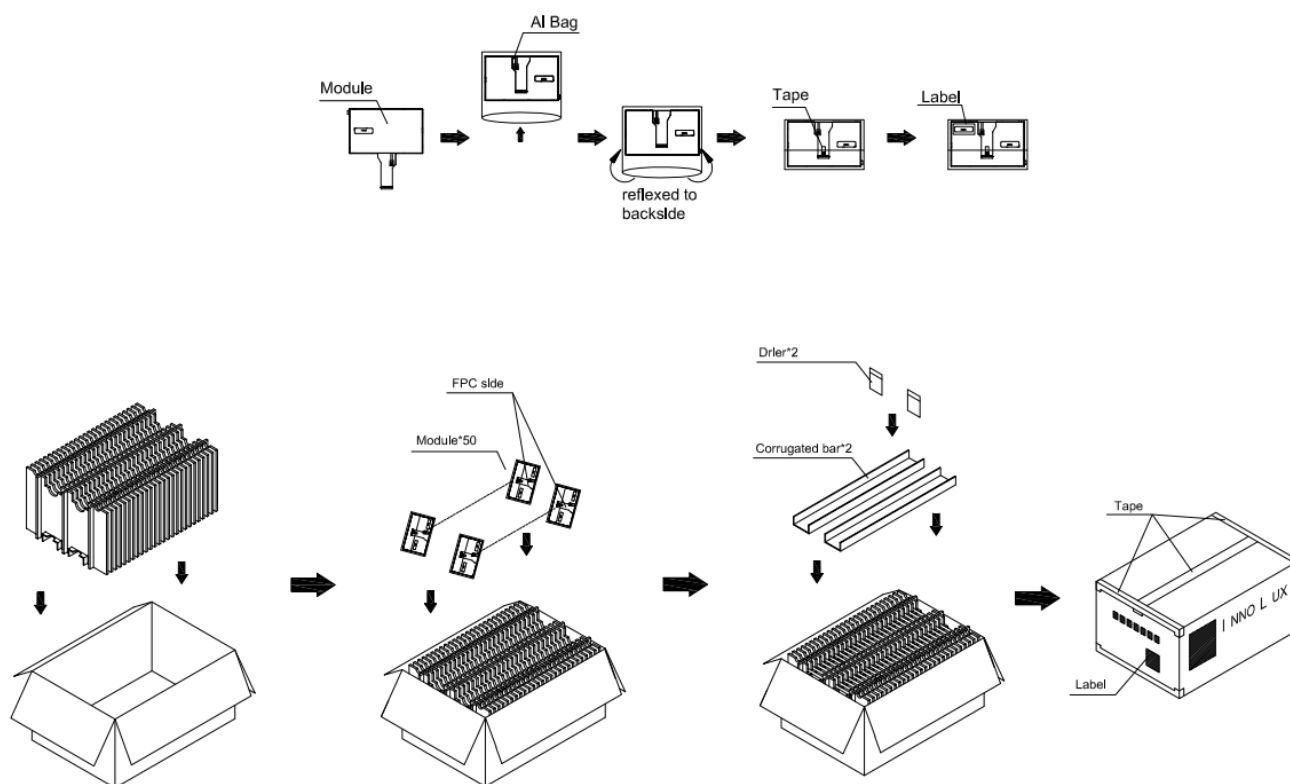
6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.



INNOLUX Package Drawing

Date :2021/11/17 Page:21/21



- (1) Box Dimensions : 535(L)*370(W)*265(H)
(2) 50 Modules/Carton